

## APPARATUS AND METHOD OF MAKING A SLIP FREE WAFER BOAT

### CROSS REFERENCE TO RELATED APPLICATION

[0001] This application claims priority to pending provisional application Serial No. 60/245420, filed November 2, 2000, herein incorporated by reference in its entirety.

### FIELD OF THE INVENTION

[0002] The present invention relates to semiconductor manufacturing. More specifically, the present invention relates to wafer boats having wafer support structures, such as slots, that are coated with a protective ceramic coating and subjected to a post coating finishing process to define a wafer contact surface having a post coating surface finish for each slot.

### BACKGROUND OF THE INVENTION

[0003] Although other materials may be used, e.g., Silicon-Germanium (SiGe) or Gallium Arsenide (GaAs), Silicon (Si) is presently the most important semiconductor for the electronics industry. Very Large Scale Integrated (VLSI) circuit technology (i.e., up to about 100,000 devices per chip), and Ultra Large Scale Integrated (ULSI) circuit technology (i.e., more than 100,000 and in some cases exceeding one billion devices per chip) being based almost entirely on silicon.

[0004] It is critical that the fabrication of VLSI and ULSI circuits which take place on silicon substrates possess very high crystalline perfection or purity. That is, in crystalline solids, the atoms which make up the solid are spatially arranged in a periodic fashion. If the periodic arrangement exists throughout the entire solid, the substance is defined as being formed of a single crystal. The periodic arrangement of the atoms in the crystal is called the lattice. Very high crystalline perfection requires that the silicon substrate possess a minimum of impurities and structural defects throughout its single crystal silicon lattice.

09884720.061901

[0005] Generally, raw material, e.g., quartzite, is refined into electronic grade polysilicon (EGS) and melted. A silicon seed crystal is then used to grow a single crystal silicon ingot from the molten EGS. The ingot is then precisely sliced and polished into silicon wafers. The silicon wafers provide the substrates upon which VLSI and ULSI circuits are ultimately built through a complex sequence of wafer fabrication processes.

[0006] The increasing size of silicon wafers is one of the most obvious trends in silicon material technology. Presently, 300mm diameter wafers are expected to ultimately replace most 150 mm and 200 mm wafer applications. It is also predicted that 400 mm wafers will probably be introduced in the not too distant future. The use of larger diameter wafers for maintaining productivity presents several major challenges to semiconductor manufacturers. For example, facilities with equipment capable of handling the larger wafers, e.g., vertical furnaces, must be built. New patterning techniques must be developed to print smaller feature sizes over larger areas. The larger wafers must also be thicker to increase their resistance to warping and other structural deformations. Moreover, the larger wafers are also heavier, requiring the use of automated wafer transport systems.

[0007] As the silicon wafers become bigger and heavier, the problem of preventing impurities and structural defects to the lattice, i.e., of maintaining very high crystalline perfection, becomes even more critical. One such structural defect, which becomes especially problematic in 300 mm silicon wafers and larger, is that of slip in the lattice structure.

[0008] Slip in silicon wafers is a function of the stress applied to the wafer. This stress can be mechanical (e.g., frictionally induced) and/or thermal. As the wafers are stressed, the crystal lattice undergoes elastic deformation that disappears as the solid crystal returns to its original position upon release of the stress. However, severe stress leads to slip, which is the plastic or permanent deformation in the crystal lattice, which remains when the stress is released. Slip occurs when the elastic limit (or

yield strength) of the silicon is exceeded and the lattice becomes permanently misaligned.

[0009] Slip is common during high temperature processing of silicon wafers in heat treatment furnaces (furnacing operations), as thermal stress is proportional to the processing temperature. The transition temperature from brittle to ductile behavior of the wafer is generally within the range of about 720 to 1000 degrees C. Therefore slip, whether induced by thermal or mechanical stress, becomes especially problematic at process temperatures above 720 degrees C.

[0010] Wafer boats are wafer support devices, which are subjected to furnacing operations during semiconductor wafer processing. Horizontal wafer boats are typically designed to support a horizontal row of wafers, which are inserted into a horizontal furnace tube for high temperature processing. Vertical wafer boats are typically designed to support a vertical stack of wafers, which are inserted into a vertical furnace tube. Generally, for large diameter silicon wafers, e.g., 300mm, vertical wafer boats are more commonly used. This is because vertical furnaces have a smaller foot print than horizontal furnaces and therefore take up less of the expensive manufacturing space. Additionally, vertical furnaces generally demonstrate better temperature control than horizontal furnaces.

[0011] Wafer boats are generally composed of ceramic materials. Ceramic materials, which are joined by ionic or covalent bonds, are typically composed of complex compounds containing both metallic and non metallic elements. Ceramics typically are hard, brittle, high melting point materials with low electrical and thermal conductivity, good chemical and thermal stability, and high compressive strengths. Examples of ceramic materials are quartz, silicon carbide (SiC) and recrystallized SiC. One such recrystallized SiC is available

from Saint-Gobain Ceramics & Plastics, Inc. of Worcester, MA, under the trade name CRYSTAR®. This material is a silicon carbide ceramic that has been impregnated with high purity silicon metal.

[0012] The surfaces of the wafer boats which come into contact and support the weight of the silicon wafers are typically called wafer contact surfaces. In wafer boats designed to hold a plurality of silicon wafers, the wafer contact surfaces are typically disposed in wafer slots of the wafer boats, which are sized to receive each silicon wafer. In wafer boats designed to hold a single wafer (more commonly used for large diameter wafers) the wafer contact surfaces may be located on other structures of the wafer boats, e.g., raised wafer support pads. The frictional forces between these wafer contact surfaces and the silicon wafers during heating and cooling, i.e., thermal operations, are a source of mechanical stress, which can induce slip. Because common ceramic materials used for boats have different thermal expansion coefficients than single crystal silicon, the wafers detrimentally slide on the wafer contact surfaces during thermal cycling, thereby increasing mechanical stress and potentially inducing slip. This is especially the case for the heavier 300 mm wafers during furnacing operations which exceed 720 degrees centigrade.

[0013] Another problem associated with wafer boats is that it is very difficult and expensive to maintain the high purity levels required for electronic grade silicon processing over the entire body of the boats. That is, at high temperatures, impurities in the body of the boats, e.g. iron, copper, nickel, aluminum, sodium, calcium or the like, can migrate and contaminate the wafers. To prevent this, the boats are dimensionally built undersized and coated, via a chemical vapor deposition (CVD) process, with a protective coating of high

purity ceramic such as SiC. Typically, the SiC coating is nominally 60 microns thick and must be a minimum of 30 to 40 microns thick in order to adequately prevent the migration of impurities through the coating. The purity levels required within the coating itself are generally 1 part per million (ppm) or less.

[0014] Problematically however, CVD coated boats generally have a surface finish of greater than 1.0 microns. For purposes of this application all surface finishes will be designated in microns and shall represent the standard maximum roughness height index, i.e., arithmetic average, normally designates by the symbol "Ra". For the 300 mm and larger wafers, this relatively rough surface finish can result in excessive frictional forces at the higher temperature operations, which can induce slip.

[0015] In an attempt to solve the problem of slip for the larger wafer sizes, prior art boats have been built without a CVD coating, and the wafer contact surfaces machined to a highly smooth surface finish, e.g., less than 0.4 microns, to reduce friction. However, control of the purity levels over the entire body of such boats has proven to be difficult to maintain at required levels, e.g., 1ppm or less. As a result, these prior art boats are excessively expensive. Additionally, it is questionable whether the purity of these prior art boats can meet the rigorous standards required to prevent the migration of impurities into the silicon wafers, which would compromise the wafer's very high crystalline purity.

[0016] Accordingly, there is a need for an improved wafer boat, which can minimize the frictional forces that cause slip in large diameter silicon wafers, and which maintains the purity levels required for silicon wafer processing at a reasonable cost.

SUMMARY OF THE INVENTION

[0017] The present invention offers advantages and alternatives over the prior art by providing a wafer boat having wafer support structures, e.g., slots, that are coated with a protective ceramic coating and subjected to a post coating finishing process to define a wafer contact surface having a post coating surface finish for each support structure. The ceramic coating has a predetermined thickness, which substantially prevents migration of impurities from the wafer boat to the silicon wafers being processed, i.e., an impurity migration preventing thickness. Additionally, the post coating surface finish of the wafer contact surfaces substantially prevents slip from occurring. Because only the purity of the coating has to be maintained at a very high purity level, as opposed to the entire body of the boat, the wafer boat can be manufactured at a reasonable cost.

[0018] These and other advantages are accomplished in an exemplary embodiment of the invention by providing a wafer boat for supporting silicon wafers. The wafer boat includes a ceramic body having at least one wafer support structure sized to support a silicon wafer thereon. A ceramic coating is disposed on a surface of the wafer support structure. The ceramic coating has an impurity migration preventing thickness and a wafer contact surface. The wafer contact surface has a post coating surface finish, which substantially prevents slip in the silicon wafers.

[0019] The wafer boat is typically a vertical wafer boat, having wafer support structures which are slots sized to receive silicon wafers of 300 mm diameter or greater. Embodiments of this invention are especially effective in preventing slip in large sized silicon wafers during thermal operations, which reach temperatures of 720 degrees centigrade or greater.

[0020] An exemplary embodiment of a method of making the invention includes a process of providing a ceramic wafer boat body having at least one wafer support structure sized to support a silicon wafer thereon. A coating process is then utilized to coat a surface of the wafer support structure with a protective

FOOTNOTES

[0021] An embodiment of the coating process includes a chemical vapor deposition (CVD) process. An embodiment of the finishing process includes a machining operation and/or a laser cutting operation.

10022] Fig. 1 is a perspective view of an exemplary embodiment of a vertical wafer boat in accordance with the present invention;

[0024] Fig. 3 is an enlarged view of the circular section E of Fig. 2;

[0026] Fig. 5 is an exemplary embodiment of a manufacturing flow chart for making a wafer boat in accordance with the present invention.

[0027] Referring to Figs. 1 and 2, a vertical wafer boat in accordance with the present invention is shown generally at 10. The entire wafer boat 10, i.e., the body of the wafer boat, is composed of cast recrystallized SiC and includes three support rods 12, a base 14 and a top plate 16.

[0029] The support rods 12 are elongated structures having a generally rectangular shaped cross section. The outer edges 27 of the support rods 12 are flush against the outer periphery of the base plate 18 and the support rods 12 are oriented on the base plate 18 to receive a predetermined diameter silicon wafer, e.g., 300 mm. The rods 12 are welded to the upper surface of the base plate 18 at their lower distal ends 28 and extend substantially vertically upwards therefrom.

[0030] The upper distal ends 30 of the support rods 12 are welded to the lower surface of the top plate 16, which is a U shaped flat plate that extends substantially parallel to the base plate 18. The U shape of the top plate 16 allows for expansion and contraction during high temperature thermal operations in much the same way as the expansion slot 22 does for the base 14. Though the support rods 12, base 14 and top plate 16 are described as being welded together, other assembly techniques are also within the scope to this invention, e.g., bolting, press fitting or chemically bonding the components together.

[0031] Referring to Fig. 3, the support rods 12 include a plurality of ceramic arms 32. The ceramic arms 32 include an anchored end portion 35 proximate the outer edge 27 of the support rod 12. The ceramic arms 32 extend from the



anchored end portion 35 in the general direction of the center portion of the base plate 18 and generally parallel to the base plate 18. Each pair of arms 32 defines a wafer slot 34 sized to receive a silicon wafer of predetermined diameter, e.g., 300mm, and associated thickness. For purpose of this invention, an arm is to be considered any structure with a surface which contacts, and supports, the weight of a silicon wafer (wafer contact surface). Therefore, that arm portion 33 of the support rods 12 which extends below and defines the lower most slot 34, is also to be considered an arm. The wafer boat's body 10 is molded such that it is dimensionally undersized in at least its critical dimensions, e.g., the areas around the slots 34, in order to leave room for a CVD protective coating 36 of high purity SiC, e.g., less than 1 ppm. Though this embodiment describes a wafer boat having slots with wafer contact surfaces designed to hold a plurality of wafers, one skilled in the art would recognize that other structures of the wafer boats may also have wafer contact surfaces. For example, a wafer boat designed to hold a single wafer may include raised pads on a ring, in which the top surfaces of the pads are the wafer contact surfaces.

[0032] The protective coating 36 generally includes a surface finish of greater than 1.0 micron throughout the body of the wafer boat 10. However, the CVD coating 36 disposed on the lower surface of each wafer slot 34 is subjected to a post coating finishing process, e.g., machined or laser cut, to remove a predetermined amount of ceramic coating. Accordingly, this finishing process provides a protective coating on each ceramic arm 32 and 33 having a nominal thickness of approximately 60 microns. Additionally the protective coating includes a wafer contact surface 38 for each arm 32 and 33 having a post coating surface finish, i.e., a surface finish obtained from the post coating finishing process, of approximately 0.4 microns. Advantageously, the SiC protective coatings are thick enough, e.g., a minimum of 30 microns, to prevent impurities in the body of the wafer boat 10 from migrating through the coating 36 and into the silicon wafers (not shown) during a furnacing operation. Additionally, the post coating surface finish of each wafer contact surface 38 reduces the coefficient of friction between the wafer contact surface 38 and the silicon wafer to substantially eliminate the possibility of slip occurring during high temperature thermal operations, i.e., operations in which the temperature

reaches 720 degrees centigrade. Though this embodiment shows the protective coating 36 being applied to the entire body of the wafer boat 10, it is within the scope of this invention to apply the protective coating 36 to just the areas around the slots 34 where the wafer contact surfaces 38 will be located.

[0033] Though a CVD protective coating of SiC is shown in this embodiment, one skilled in the art will recognize that other ceramic coatings may be used, e.g., silicon nitride ( $\text{Si}_3\text{N}_4$ ). Additionally, the protective coating thickness may also vary so long as it performs its primary function of preventing impurities from migrating through to the silicon wafer, i.e., an impurity migration preventing thickness. Additionally, the post coating surface finish of the wafer contact surfaces may vary from the 0.4 microns discussed above, so long as the coefficient of friction is reduced to substantially prevent slip, especially during high temperature thermal operations.

[0034] Though the wafer boat 10 is described above as being of the vertical type, it will be clear to one skilled in the art that horizontal wafer boats are within the scope of this invention. Also, although this exemplary embodiment discusses a wafer boat sized for 300mm diameter wafers, a wafer boat in accordance with this invention may be sized for other wafer diameters as well, e.g., 150mm, 200mm, or 400mm. Additionally the wafer boat may also be composed of ceramics other than recrystallized SiC, e.g., quartz, sintered SiC, or poly-silicon.

[0035] Referring to Fig. 4, an alternative embodiment of a vertical wafer boat in accordance with the present invention is shown generally at 40. In this case, wafer boat body 40 includes a base 42, a top plate 44 and a plurality of four support rods 46. Additionally, the top plate 44 is generally ring shaped and includes an expansion slot 48 to allow for expansion and contraction during high temperature thermal operations. The base 42 is generally circular shaped and includes an expansion slot 50. A set of four mounting slots 52 is formed into the outer periphery of the base 42. Upon assembly, the support rods 46 are press fit into each mounting slot 52 such that the lower distal ends of the rods 46 are flush with the bottom surface of the base 42.

[0036] Referring to Fig. 5, a typical manufacturing process flow chart in accordance with the present invention is shown generally at 60. Raw SiC grain 62 is mixed with de-ionized water to form a SiC slurry 64. The slurry is poured into pre-prepared plaster molds 68 where it is then processed to produce the cast products at step 70. The cast products, i.e., the green (unfinished) support rods 12, base 14 and top plate 16, are then removed from the molds and are put through a green finishing operation 72, which brings the parts closer to their final net shape. After green finishing, the individual parts are subjected to a recrystallization process 74. That is the green finished parts are subjected to a high temperature firing operation wherein the ceramic grains are bonded together of sintered to form blanks. A pre-machine inspection of the blanks 76 is then performed to check for damaged or out of tolerance parts. If required, the blanks are machined 78 to provide further detailed structure and to meet proper dimensional tolerances. The unfinished parts are then cleaned in an acid bath 80 to remove impurities. A pre-assembly inspection of the components 82 is then performed on the components of the boat 10. Next the support rods 12, base 14 and top plate 16 are put through an assembly process 84, e.g., welding, bolting, press fitting or chemically bonding, to form an unfinished wafer boat 10. The wafer boat 10 is then subjected to a silicon impregnation process 86, in which the SiC ceramic wafer boat 10 is impregnated with high purity silicon metal to fill any porous cavities and bring the wafer boat 10 up to its full density. After silicon impregnation 86 the surface of the wafer boat 10 is sandblasted to remove the impurities and excess silicon 88. Since sand blasting roughens the surfaces of the wafer boat 10, the critical features of the wafer boat are then subjected to another machining operation 90 to bring the critical dimensions of the boat 10 into tolerance.

[0037] It is essential, at this point, that the ceramic boat 10 be dimensionally undersized by a predetermined amount in the critical dimensions of the boat 10, especially where the surfaces of the boat 10 come in contact with and support the silicon wafers, i.e., the wafer contact surfaces 38. This is because room must be allowed for the application of a protective coating, in this case a high purity

CVD silicon carbide coating. Therefore, the machined features of the boat are subjected to an inspection process 92 to confirm the accuracy of the machining process. The CVD SiC coating 94 is then applied to prevent impurities in the body of the boat 10 from migrating into the silicon wafers. Typically the SiC coating is applied up to a 100 micron nominal thickness over the entire body of the boat 10.

[0038] Advantageously, the wafer contact surfaces 38 are subjected to a post coating finishing process 96, e.g., machining or laser cutting, which removes a predetermined amount of coating and finishes the wafer contact surfaces 38 to approximately 0.4 microns Ra (a post coating surface finish). Typically in this step 96 the protective CVD SiC coating is reduced to a thickness of 60 microns nominal. The protective coating must, in any case, have a predetermined minimum thickness in order to adequately prevent the migration of impurities. This minimum limit is generally 30 to 40 microns for CVD SiC coatings. The post coating surface finish must also be below a predetermined limit in order to reduce the coefficient of friction between the wafer contact surfaces 38 and the silicon wafers to a point which essentially prevents slip. Typically, the post coating surface finish must be at least less than 0.4 micron.

[0039] After the post coating finishing process 96, the wafer boat 10 is subjected to a final inspection 98. The finished wafer boat is then given a final acid cleaning, packaged and prepared for shipment 100.

[0040] Though the manufacturing process 60 in this embodiment has been described as primarily a casting operation, one skilled in the art would recognize that other manufacturing processes may also be utilized to manufacture the wafer boat 10. For example, the wafer boat manufacturing process may be primarily a machining, pressing or extrusion process.

[0041] While preferred embodiments have been shown and described, various modifications and substitutions may be made thereto without departing from the spirit and scope of the invention. Accordingly, it is to be understood that the present invention has been described by way of illustration and not limitation.

09384720.061901  
T06T90.02/48860